



Patent Application No. 10/820,396  
Customer Number: 42717

IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Chung-Hui Chen, et al.	§	Docket No.:	TS01-1339 (24061.420)
Serial No.:	10/820,396	§	Examiner:	Tuyen T. Nguyen
Filing Date:	April 8, 2004	§	Art Unit:	3729
For:	Deep Submicron CMOS	§	Conf. No.:	8456
	Compatible Suspending Inductor	§		
		§		

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop: Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The present paper is being submitted in response to the Restriction Requirement Office Action dated February 9, 2006 in the above-identified application.

**Election of Claims** begins on page 2 of this paper.

**Amendments to the Claims** are reflected in the Listing of Claims which begins on page 3 of this paper.

**Remarks** begin on page 10 of this paper.

**I. Election**

In the Office Action mailed February 9, 2006, the Examiner alleges that the application contains claims directed to two inventions and, thus, required restriction of either:

Group I: Claims 1-28, drawn to a method of creating an inductor; or

Group II: Claims 29-45, drawn to an inductor.

In response, Applicants hereby elect Group I, corresponding to claims 1-28. Applicants' election is made with traverse on the grounds that the embodiments delineated by the Examiner are not patentably distinct and therefore constitute a single invention concept.